Milestones

TABLE 2. MILESTONES							
Milestone no.	Milestone name	Work package no	Lead beneficiary	Delivery date from Annex I dd/mm/yyyy	Achieved Yes/No	Actual / Forecast achievement date dd/mm/yyyy	Comments
MS21	Decision on the continuation of Task 2.4	WP2	Fraunhofer	30/06/2011	Yes	30/06/2011	Verification: Possibility to build one or several models
MS22	First version of process models developed in Tasks 2.1 to 2.3	WP2	Fraunhofer	30/06/2012	No	30/06/2012	Verification: Models communicated to WP6
MS23	Process models developed in Tasks 2.1 to 2.3	WP2	Fraunhofer	31/12/2012	No	31/12/2012	Verification: Models transferred to WP6
MS31	Dedicated experiments designed (first lot)	WP3	UNEW	28/02/2011	No	31/08/2011	(* see below) Verification: Experimental Plan, partners and schedule
MS32	Dedicated experiments designed (final lot)	WP3	UNEW	31/08/2011	No	31/08/2011	Verification: Experimental Plan, partners and schedule
MS33	First version of process models for n+ and p+ junctions	WP3	SYNG	30/06/2012	No	30/06/2012	Verification: Models communicated to WP6
MS34	Process models for n+ and p+ junctions finalized	WP3	SYNG	31/12/2012	No	31/12/2012	Verification: Models transferred to WP6

MS41	First version of process models for excimer laser, PIII and low/high temperature implants	WP4	STM	30/06/2012	No	30/06/2012	Verification: Models communicated to WP6
MS42	Models for excimer laser, PIII and low/high temperature implants	WP4	STM	31/12/2012	No	31/12/2012	Verification: Models transferred to WP6
MS51	Validation of DLTS structures and first identification of extended defect-related DLTS spectra	WP5	CNRS	30/06/2011	Yes	30/06/2011	Verification: Structures available and characterized
MS52	First version of leakage current models	WP5	ETH Zurich	30/06/2012	No	30/06/2012	Verification: Models communicated to WP6
MS53	All junctions characterized in terms of leakage currents	WP5	SEMILAB	30/06/2012	No	30/06/2012	Verification: Leakage current measurements accomplished
MS54	Models for leakage current models finalized	WP5	ETH Zurich	31/12/2012	No	31/12/2012	Verification: Models transferred to WP6
MS61	Test applications devices fabricated and characterized	WP6	STM	31/12/2011	No	31/12/2011	Verification: Characterization results available

(*) MS31 will be merged with MS32 “Dedicated experiments designed (final lot)”. In order to assure optimal allocation of resources for experiments, it appears appropriate to decide on the experimental plan and to define the conditions for the main lot of experiments after the evaluation of short-comings and ambiguities of state-of-the-art TCAD models (D3.1 due 31/07/2011, D3.2 due 31/08/2011, both on track) is completed.