Design Environment for Global ApplicationS (DEGAS)

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Università di Verona

Consortium

- **Academic partners**
  - **Università di Verona (Coordinator)**
  - **The Technical University of Denmark**
  - **Università di Pisa**
  - **The University of Edinburgh**
- **Industrial partners**
  - **Motorola Technology Center Italy**
  - **Omnys Wireless Technology**

Scientific objectives

- **Spec. by picture and formal methods**
  - **UML vs. process algebras for mobility**
- **Static and dynamic analysis**
  - **Performance and security issues of GA**
- **Wireless TC applications**
  - **Specification and analysis**

Technological objectives

- **Enhancement of eng. principles**
  - **Tool integration and hiding of details**
- **Prototypes to validate ideas**
  - **Static and dynamic analyzers**
- **Wireless TC applications**
  - **Demos and process plan**

Tools and techniques

- **Specification**
  - **UML, (stochastic) process calculi, Argo**
- **Analysis**
  - **EOS, CFA, AI, Equivalences**
- **Case studies**
  - **Mobile entertinment and Voice interfaces**

Milestones and expected results

- **Extensions to UML to model GA**
- **Extractors from UML to PA**
- **Models and tech. to analyse GA**
- **Reflectors of PA analyses to UML**
- **Prototype design environment**
- **Enhancement of GC understanding**