



# Advanced Processor Core for Space Exploration

## **Results in Brief**

# Computing power for the next generation of space explorers

EU-funded researchers have developed a pioneering processor to provide computing power for the next generation of space missions. These include explorations of Venus and Mars as well as the moons of Saturn and Jupiter.





© Vadim Sadovski, Shutterstock

A significant amount of computing power is required on board modern exploratory spacecraft in order to carry out precise operations at incredible distances, including planet entry descent and landing manoeuvres; guidance navigation and control as well as high rate data processing in science instruments. A key limitation is the fact that space-grade processors, which are so crucial to the functioning of the spacecraft, are now at the very limits of their capabilities. If mankind

is to continue pushing the frontiers of space exploration, then new computing capabilities must be found.

The EU-funded APEX project, which was completed in September 2016, has made significant progress in this respect. This was achieved by focussing on ARM processors, which are at the heart of the vast majority of smartphones, tablet computers and embedded devices on Earth.

#### Fine tuning commercial technology

'Space-qualified computing technology lags several technology generations behind commercial devices, such as ARM processors,' explains APEX principal investigator Dr Xabier Iturbe from ARM in the UK. In fact, the vast majority of space-grade processors are manufactured using radiation-hardened process technology, which leads to a notable increase in cost, power consumption and a significant reduction in performance. 'We set out to design a fault-tolerant, energy-efficient and highperformance ARM processor prototype that could be used as an embedded processor in NASA science instrument payloads,' continues Iturbe.

The APEX project began by achieving a better understanding of the computing and fault-tolerance requirements of future NASA space exploration missions, and then adapted a commercial ARM processor that is currently used in terrestrial transportation applications to meet these requirements. The team combined some well-established fault-tolerant techniques at the processor architecture level, such as lock-stepping, and developed novel energy-efficient techniques at the processor micro-architecture level. For instance, the team came up with a 'high-resilience' processor mode to execute high-criticality software routines using the least possible amount of micro-architecture components, thus reducing the vulnerabilities of the processor without impacting the performance.

The technical feasibility of this approach was demonstrated through a number of simulations, which underlined the high resilience and significant increase in performance delivered by the processor developed in APEX. Namely, it recovers from most faults provoked by radiation within microseconds, and delivers about 1.5 times more computing power than currently existing space-qualified processors.

#### Next step: deployment

'We expect that the technology developed in this project will enable the ARM ecosystem to build radiation tolerant solutions for space and achieve higher levels of safety integrity in terrestrial applications,' says Iturbe. A number of space agencies like NASA, ESA (European Space Agency) and space equipment manufacturers like Airbus have already shown an interest in using this technology if it becomes commercially available.

'The effort done in the APEX project for developing fault-tolerant, energy-efficient and high performance computing technology using commercial processors could help improving instrument avionics and increase dramatically the science returns of NASA missions', states Dr Didier Keymeulen, principal member of technical staff at NASA's Jet Propulsion Laboratory (JPL) - California Institute of Technology and JPL scientist in charge for APEX. In fact, NASA has recently confirmed the development of an

ARM-based high performance spaceflight processor, which will be used in both manned and unmanned spacecraft in the next years.

In parallel with this, the ARM University Programme, in collaboration with ESA, is creating a prototypic solution using the APEX technology to enable universities worldwide to build their 'CubeSats', miniaturised low-cost satellites for space research that can be put into Earth orbit.

## Keywords

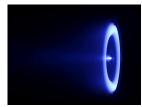
APEX, Satellite, ARM, processor, Jupiter, Saturn, space, Venus, radiation, NASA, ESA

## Discover other articles in the same domain of application



Modelling Galilean moon atmospheres finds evidence that Galileo detected a water plume on Europa





High-power thrusters fuelling future spaceflight





#### Advanced helicon plasma thruster propulsion moves closer to market



#### Research reveals new clues about planets born around two suns



**Project Information** 

APEX

Grant agreement ID: 627579

Project closed

Start date 15 September 2014 End date 14 September 2016

**Funded under** 

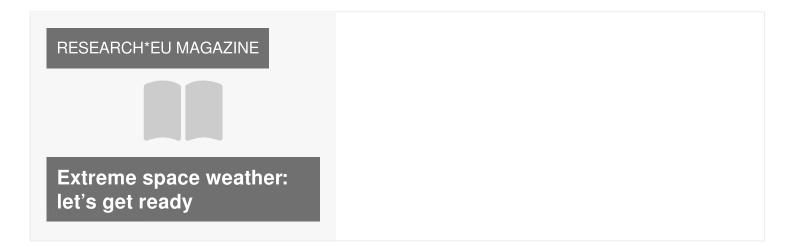
Specific programme "People" implementing the Seventh Framework Programme of the European Community for research, technological development and demonstration activities (2007 to 2013)

**Total cost** € 196 456,20

**EU contribution** € 196 456,20

Coordinated by ARM LIMITED

## This project is featured in...



Last update: 3 March 2017

**Permalink:** <u>https://cordis.europa.eu/article/id/191259-computing-power-for-the-next-generation-of-space-explorers</u>

European Union, 2025