

1. PUBLISHABLE SUMMARY

The objective of the IDESA Support Action is to develop and make available didactic training material on the design flow for integrated circuits for advanced deep sub-micron technologies, free of intellectual property rights, for the benefit of European academia.

The IDESA project addresses training of academic staff (professors, assistant professors and lecturers) from all interested European academia. New didactic material was developed and made available for reuse in the course portfolio of bachelor and master engineering curriculum. Training was and is organised in class-based hands-on sessions and advanced seminars using state-of-the-art multimedia technology over the web or on DVD.

- **4 different advanced implementation courses** are touring different sites in Europe. These courses are hands-on courses using a train-the-trainer philosophy. Each of these courses will be repeated 7 times, to reach a significant proportion of the 600+ European academia. All of these courses address advanced implementation issues relating to the 90 nm nodes. This brings the universities to a more advanced level of implementation skills to start engaging in 65- and 45-nm design projects.

Sessions for all the courses have been organised and were attended by representatives from over 40 different academia. These are:

- Advanced analog implementation flow: 3 sessions
- Advanced RF implementation flow: 4 sessions
- Digital Physical implementation flow: 3 sessions
- Design-for-manufacturing: 3 sessions

The calendar has been expanded with 6 additional course in: Czechia, Slovakia, Poland and Belgium.

- The consortium started building a portfolio of **public domain didactic seminars**, addressing issues that are not addressed in the design flow courses but that will start to be of dominant importance for the 65 and 45 nm generation design flows. This seminar portfolio will be **supplemented with fully documented didactic lab exercise-material** that can be reused free of intellectual property rights in the curricula of European engineering students. The goal is to cover 32 topics in this portfolio, during the 30 months duration of the project, with 5 didactic lab exercises.

A Program Board consisting of representatives of the industry (both IDM and EDA) and of the Education and Training Working Group (ETWG) of ENIAC has been set up. The Program Board helps define the content of the 65- and 45-nm seminar portfolio and advises on how to best guide the transition from a 90-nm focus to a 65- and 45-nm.

18 Didactic seminars are available to the public, both in streaming media format, or as downloadable multimedia formats.

The project website is available on www.idesa-training.org

2. PROJECT OBJECTIVES FOR THE PERIOD - BDM

2.1 The project Objectives:

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