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Collaborative project-

Project acronym: SNM

## Project full title: "Single Nanometer Manufacturing for beyond CMOS devices"

Grant agreement no: 318804

## Deliverable: 7.1 ("E-beam transparent Si<sub>3</sub>N<sub>4</sub> supports")

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Participant		Part. short	Activity Type	Country
no.	Participant organisation name	name		
1 (Co)	Technische Universität Ilmenau	TUIL	HER	Germany
2	EV Group E. Thallner GmbH	EVG	IND; End-user	Austria
3	IMEC	IMEC	RES	Belgium
4	Mikrosistemi Ltd	μS	SME; End-User	Bulgaria
5	Universität Bayreuth	UBT	HER	Germany
6	Technische Universiteit Delft	TUD	HER	Netherlands
7	Spanish National Research Council	CSIC	RES	Spain
8	IBM Research GmbH	IBM	IND; End-user	Switzerland
9	École polytechnique fédérale de Lausanne	EPFL	HER	Switzerland
10	SwissLitho AG	SL	SME; End-User	Switzerland
11	Oxford Instruments Nanotechnology Tools Ltd	OINT	IND; End-user	UK
12	Imperial College London	IMPERIAL	HER	UK
13	The Open University	OU	HER	UK
14	Oxford Scientific Consultants Ltd	OSC	SME	UK
15	VSL Dutch Metrology Institute	VSL	IND	Netherlands
16	University of Liverpool	ULIV	HER	UK





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Deliverable: 3.1 ("E-beam transparent Si <sub>3</sub> N <sub>4</sub> supports")												
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beneficiary							leve	el				
number												
Estimated	12.00											
Person-												
months												
Person-	EPFL											
partner for	11.5											
the												
Deliverable												
Estimated	Month 12Delivery DateMonth 12											
Delivery	December 10 <sup>th</sup> 2012											
Date				cember	19,2013	2						
Description	E-beam transparent Si3N4 supports The goal of this deliverable was to develop a process											
of the	e-beam transparent flow for the fabrication of electron-beam											
Deliverable	Si <sub>3</sub> N <sub>4</sub> transparent silicon-nitride TEM samples for											
	$\epsilon SiO_2$ state-or the art LEW imaging and CD determination with subnanometer resolution											
	← Si of patterns produced in the SNM project. This											
	Figure 1. Schematic cross-section of an e-beam technological platform will be especially											
	transparent Si <sub>3</sub> N₄ membrane. useful for line width and line edge roughness											
	determination in patterned resist layers and transferred patterns and can also be used as a											
	back-up metrology tool during the development of high-resolution AFM –based metrological											
	LOOIS.											
	Figure 1 shows the cross-section of the membrane. They are fabricated using conventional											
	photolithography combined with wet and dry etching. Si $_3N_4$ and SiO $_2$ are first deposited on											
	commercial Si wafers. Alignment and fiducial markers are fabricated on the top side. Square											
	windows are opened on the bottom site using dry etching. The back side is further etched											
	membrane thickness presents a compromise between the need to have mechanical stable											
	membrar	nes with ar	e in the sa	me time s	sufficiently	thin	to be	electron	bean	n trar	nsparent.	The





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	membranes can further be coated with CVD graphene in order to provide an atomically thin and conductive bottom layer which may be needed for certain SNM lithographic methods.
	(a) (b) (b) (c) (c) (c) (c) (c) (c) (c) (c
	Figure 2 (a) Photograph of a 4" wafer containing 64 TEM compatible membranes. (b) A closeup view of a membrane.
	Figure 2 shows the front side of a wafer containing 64 TEM-transparent membranes, each with a dimension of 50 $\mu$ m $\times$ 50 $\mu$ m (b).
Explanation	D7.1 has been achieved in its totality.
of the	
Differences	
between	
Estimation	
and	
Realisation	