

# 1. PUBLISHABLE SUMMARY

## 1.1. *Summary description.*

The IDESA-2 project was a follow-up of the FP7 call-1 IDESA-1 support action that addresses training of academic staff (professors, assistant professors and lecturers) from all interested European academia. The sole purpose of the IDESA-2 Support Action was to continue making available didactic training material on the design flow for integrated circuits for advanced deep sub-micron technologies, free of intellectual property rights, for the benefit of European academia. Didactic material was developed and made available for reuse in the course portfolio of bachelor and master engineering curricula during IDESA-1. Training was organised in class-based hands-on sessions and advanced seminars using state-of-the-art multimedia technology over the web or on DVD.

IDESA-2 partners organised a road show of 4 different advanced implementation courses that toured different sites in Europe. These courses are hands-on courses using a train-the-trainer philosophy. Each of these courses was repeated 7 more times, to reach 100 to 140 of the 600+ European academia. All of these courses addressed the advanced implementation issues relating to the 90/65 nm process node. This brought the universities to a more advanced level of implementation skills to start engaging in 65- and 45-nm issues.

The full details of the courses listed below can be found on <http://www.mtc-online.be/mtc/idesa/Courses.html>

- Advanced analog implementation flow.
- Advanced RF implementation flow
- Advanced digital physical implementation flow
- Design-for-manufacturing

The consortium has built a portfolio of public domain didactic seminars, addressing issues that are not addressed in the design flow courses but that start being of dominant importance for the 65 and 45 nm generation design flows. The IDESA portfolio holds 43 didactic tutorials. The portfolio was established with the assistance of a Program Board consisting of representatives of the industry (both IDM and EDA) and of the Education and Training Working Group (ETWG) of ENIAC. The IDESA tutorial portfolio still is on-line and will be maintained for at least 2 more years by imec.

## 1.2. **Description of the work performed.**

The Course content of the 4 implementation courses was reviewed and updated where necessary. The agreements with hosting organisations have been made, and in total 28 sessions were scheduled. 8 additional tutorials were added to the tutorial portfolio.

## 1.3. **Expected final results.**

The final results are in line with the results outlined in the course proposal.

## 1.4. **Address of the project public website.**

- [www.idesa-training.org](http://www.idesa-training.org)