



**SEVENTH FRAMEWORK PROGRAMME THEME 3  
Information and Communication Technologies (ICT)  
ICT-2009.3.3 – Flexible, organic and large area electronics**

**POLARIC**

Printable, organic and large-area realisation of integrated circuits

**Deliverable D8.4  
Interim Management Report**

**CONFIDENTIAL**

**Responsible beneficiary:** VTT  
**Nature of deliverable<sup>1</sup>:** R  
**Dissemination level<sup>2</sup>:** PU  
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<sup>1</sup> R = Report P = Prototype D = Demonstrator O = Other

<sup>2</sup> PU = Public, PP = Restricted to other programme participants (including the Commission Services), RE = Restricted to a group specified by the consortium (including the Commission Services), CO = Confidential, only for members of the consortium (including the Commission Services)



### **Brief description of the purpose and content of the deliverable**

The objective of the project is to realise high-performance organic electronic circuits using large-area processing compatible fabrication methods. The high performance of the organic circuits referred to here means low operating voltage (below 5 V), low power consumption, low parasitic capacitance, and high speed (kHz-MHz range).

The purpose of this interim report is to describe the progress of the project during the period January-July 2011. The report describes the overall progress towards the objectives in the technical work, and more detailed overview of the progress in each work package. Also, dissemination and management activities are described.